

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of ) Art Unit: 2812  
Shunpei YAMAZAKI et al. ) Examiner: S. Isaac  
Serial No. 10/072,931 )  
Filed: February 12, 2002 )  
For: METHOD OF MANUFACTURING ) Date: October 30, 2002  
A SEMICONDUCTOR DEVICE )  
)

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12-10-02  
Payton

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**INFORMATION DISCLOSURE STATEMENT AND  
NOTIFICATION OF RELATED APPLICATION**

Commissioner for Patents  
Washington, DC 20231

Sir:

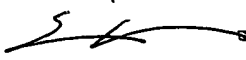
In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be considered by the Examiner and made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Applicants would like to bring to the Examiner's attention related Application Serial Nos. 10/020,961, 10/056,055, 10/066,542, 10/074,050, 10/097,641 and 10/051,064 which are listed on the form PTO-1449 herewith.

These references are being submitted concurrently with a *Request for Continued Examination (RCE)*, therefore no fee is believed to be required.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,

  
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